

AMENDMENTS TO THE SPECIFICATION

Please amend the first paragraph of the application as follows.

This is a divisional of U.S. Pat. App. No. 09/927,693, filed on August 9, 2001, now U.S. Pat. No. 6,614,685.

Please delete the prior abstract and add the following new abstract.

A Flash memory that stores data, code, and parameters and performs parallel operations employs uniform-size blocks in array planes. A redundancy system for the Flash memory uses a CAM and a RAM for address comparison and substitution when replacing addresses corresponding to defective memory elements. The uniform block size allows block replacement where spare blocks in the array planes replace defective blocks. To reduce access delays from signal propagation through the CAM and RAM, part of the input address such as the row address goes directly to decoders while another part of the input address such as the block address goes to the CAM array for comparison.

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